

### **REMARKS**

Claims 1-73 are pending in the above-identified application. Claims 1-73 were rejected. With this Amendment, no claims have been amended, added, or cancelled. Accordingly, claims 1-73 remain at issue.

#### **I. 35 U.S.C. § 102 Anticipation Rejection of Claims**

Claims 1-73 were rejected under 35 U.S.C. § 102(b) as being anticipated by Chen (U.S. Patent No. 5,553,235). Applicants respectfully traverse this rejection.

Claim 1 is directed to a method in a data processing system having a program with a plurality of threads having a plurality of states. The method comprises the step of running the program and determining the state of each thread during a measuring period. The measuring period comprises a plurality of time intervals. The method further comprises the steps of receiving user input indicating a selected one of the plurality of states; receiving user input indicating a selected one of the plurality of threads; determining a portion of the measuring period during which the selected thread is in the selected state; determining, during the portion of the measuring period, whether another thread other than the selected thread is in another state other than the selected state; and when it is determined that the other thread is in the other state, determining an amount of time that the other thread is in the other state.

Chen et al. discloses a performance tool 90 with a graphical user interface (GUI) 80 that allows a user to specify the appearance and contents of performance statistics of system resources (col. 6, lines 44-55; col. 8, lines 14-33).

In one instance, Chen et al. discloses a state graph/instrument 251 (Fig. 12e) that shows the latest statistic for a system resource (col. 22, lines 59-60). In Fig. 12e, the state

graph/instrument 251 displays a statistic for memory, paging space, and other system resources. The state graph/instrument does not show the statistics over time (col. 22, line 61).

Chen et al. also discloses a recording graph/instrument 249 (Figs. 12d and 12e) that shows statistics for a system resource over a period of time (col. 22, lines 54-56). In Fig. 12e, the recording graph/instrument 249 displays statistics for the CPU. A user can configure properties of the instrument, such as the style, foreground, background, tile patterns, interval, history, stacking, shifting, and spacing (col. 22, line 64 through col. 24, line 10). The recording graph/instrument 249, however, monitors a system resource without reference to the portion of a program, or to the state that the program is in. Column 23, and in particular lines 43-59, disclose percentages of CPU usage, but the percentages do not refer to the amount of time that a thread other than the selected thread is in the selected state or in another state.

Chen et al. does not disclose or suggest running a program with a plurality of threads having a plurality of states, where a state refers to “the portion of a program (for example, set of instructions such as subprogram, loop, or other code block) that the processor is executing during a particular time interval.” (specification, p. 2, lines 26-28). Moreover, Chen et al. is devoid of analyzing a program to find the portion of the program, or state, that the processor is executing. Furthermore, Chen et al. does not disclose or suggest: (1) running a program with a plurality of threads having a plurality of states, and then determining a time period a selected thread is in a selected state; (2) determining whether another thread other than the selected thread is in another state other than the selected state; or (3) determining an amount of time that the other thread is in the other state, all of which are required by claim 1. Because claim 1 includes limitations that are neither disclosed nor suggested by Chen et al., claim 1, and claims 2-3 that depend from

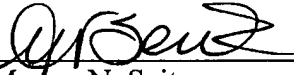
claim 1, are allowable over Chen et al. For reasons similar to those discussed with regard to claim 1, Applicants respectfully submit that claims 4-73 are also allowable over Chen et al. Accordingly, Applicants respectfully request withdrawal of this rejection.

**II. Conclusion**

In view of the above amendments and remarks, Applicants submit that all claims are clearly allowable over the cited prior art, and respectfully request early and favorable notification to that effect.

Respectfully submitted,

Dated: July 25, 2005

By:   
Marina N. Saito  
Registration No. 42,121  
SONNENSCHNEIN NATH & ROSENTHAL LLP  
P.O. Box 061080  
Wacker Drive Station, Sears Tower  
Chicago, Illinois 60606-1080  
(312) 876-8000

**IN THE DRAWINGS**

Pursuant to 37 C.F.R. § 1.121(d), enclosed is a replacement sheet for Figure 1. The changes to the drawing are to correct the drawing and do not constitute new matter. In particular, the numbering for network 190 has been corrected and the numbering for computer system 105 has been added.